

FIG. 1

PRIOR ART

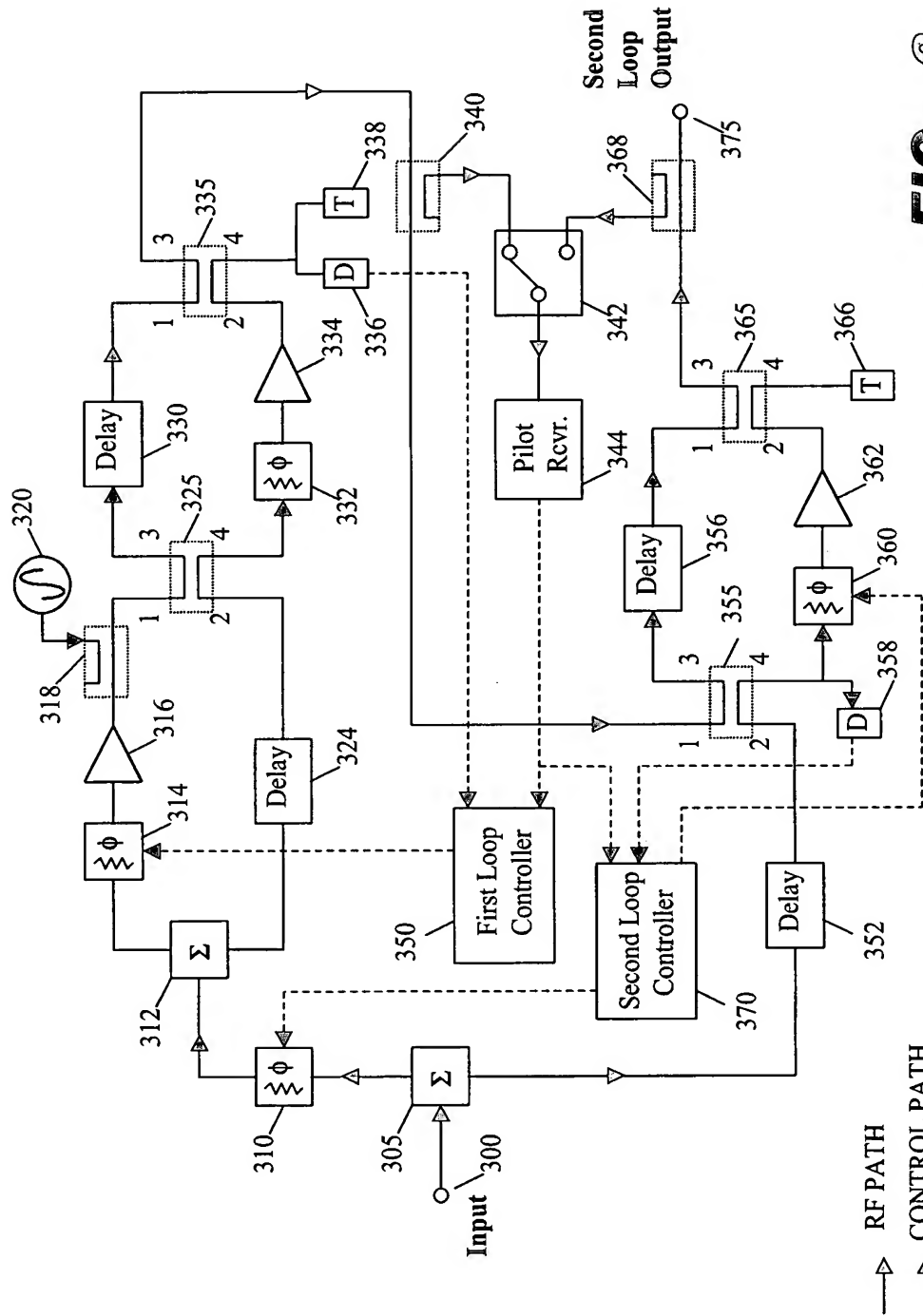
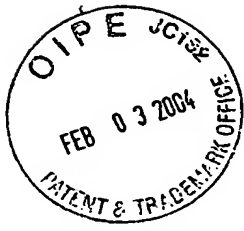


FIG. 2

PRIOR ART

RF PATH
CONTROL PATH

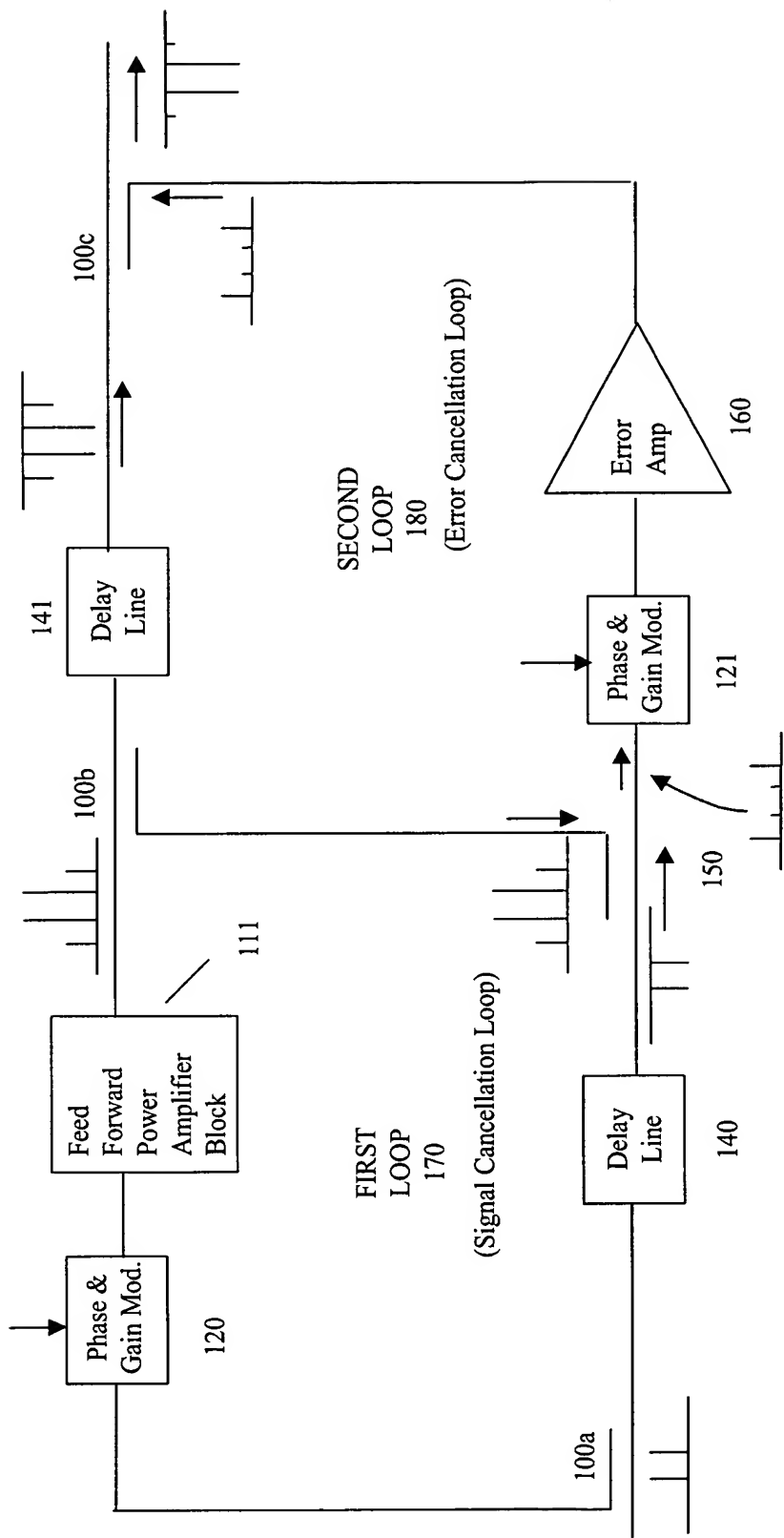
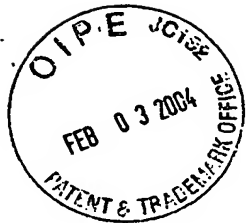


FIG. 3
PRIOR ART



FIG. 4



FIG. 5



FIG. 6



FIG. 7



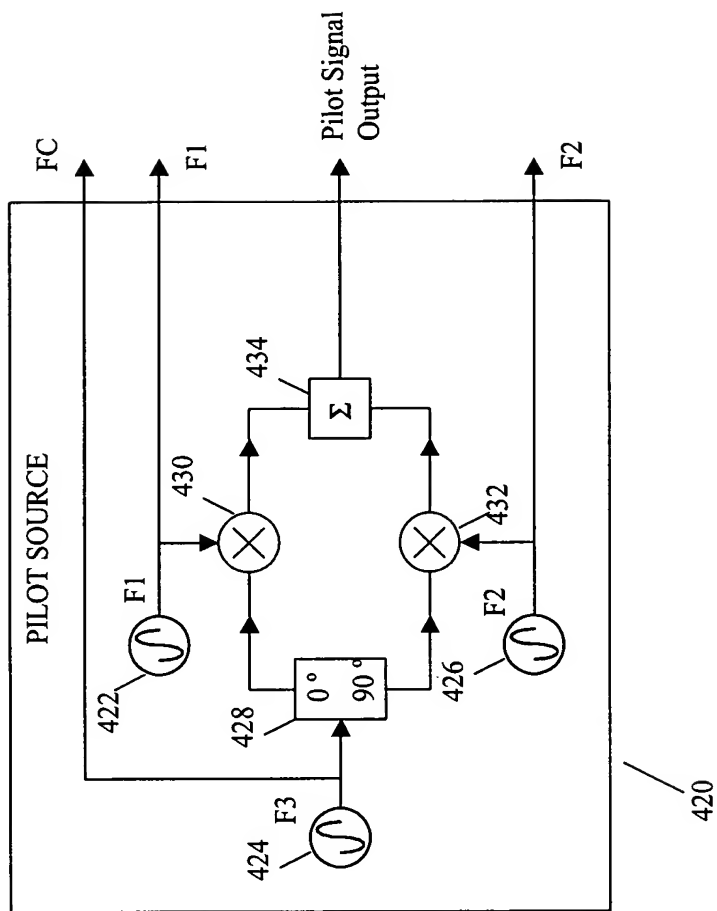


FIG. 9

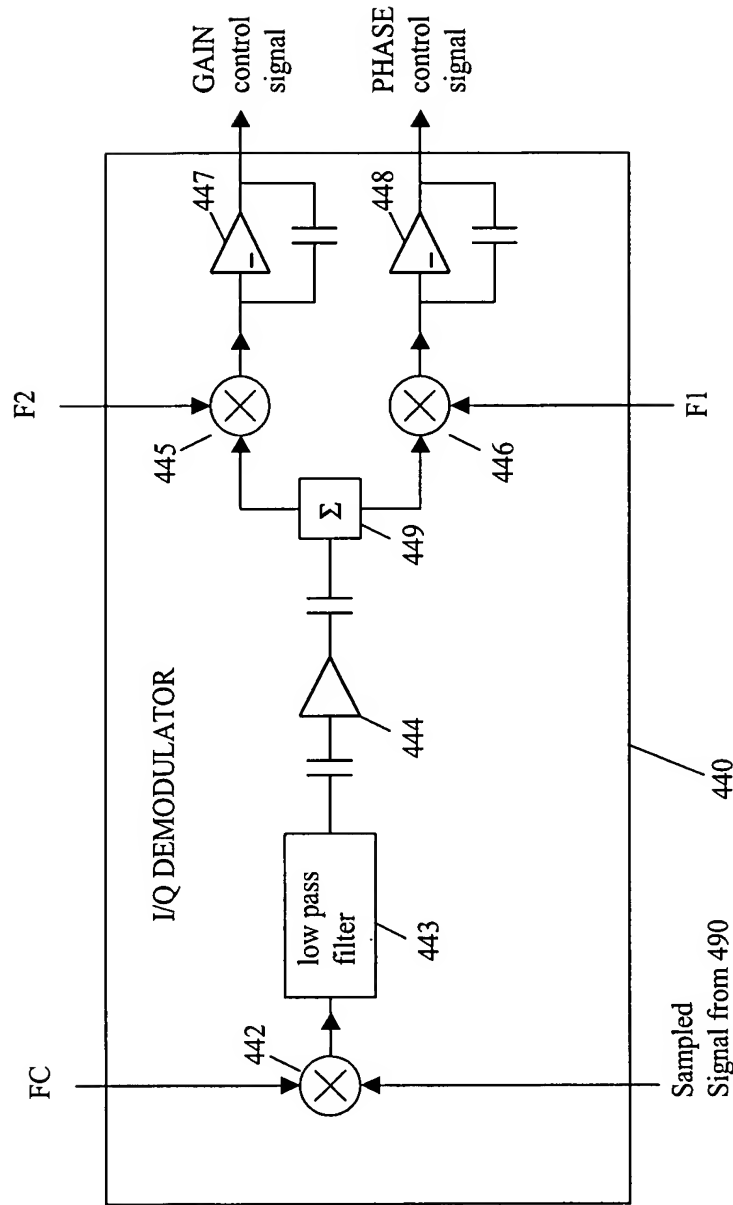
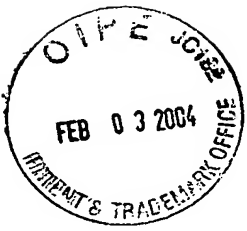


FIG. 10



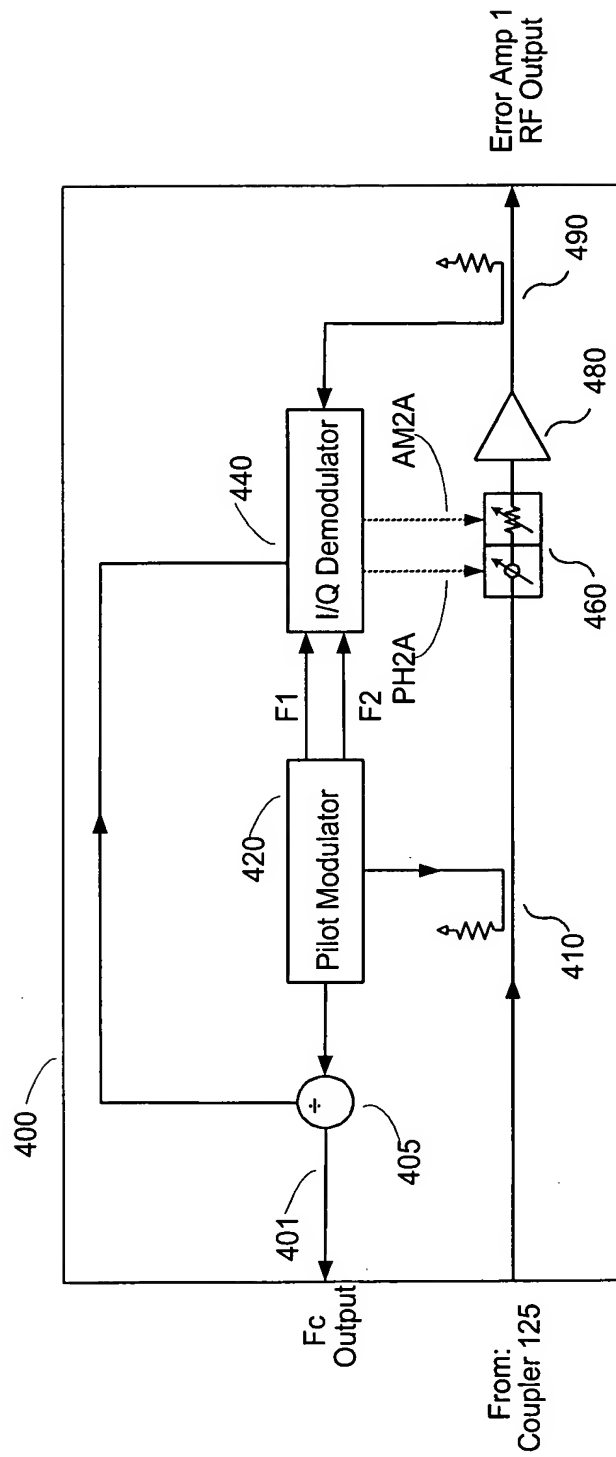


FIG. 12

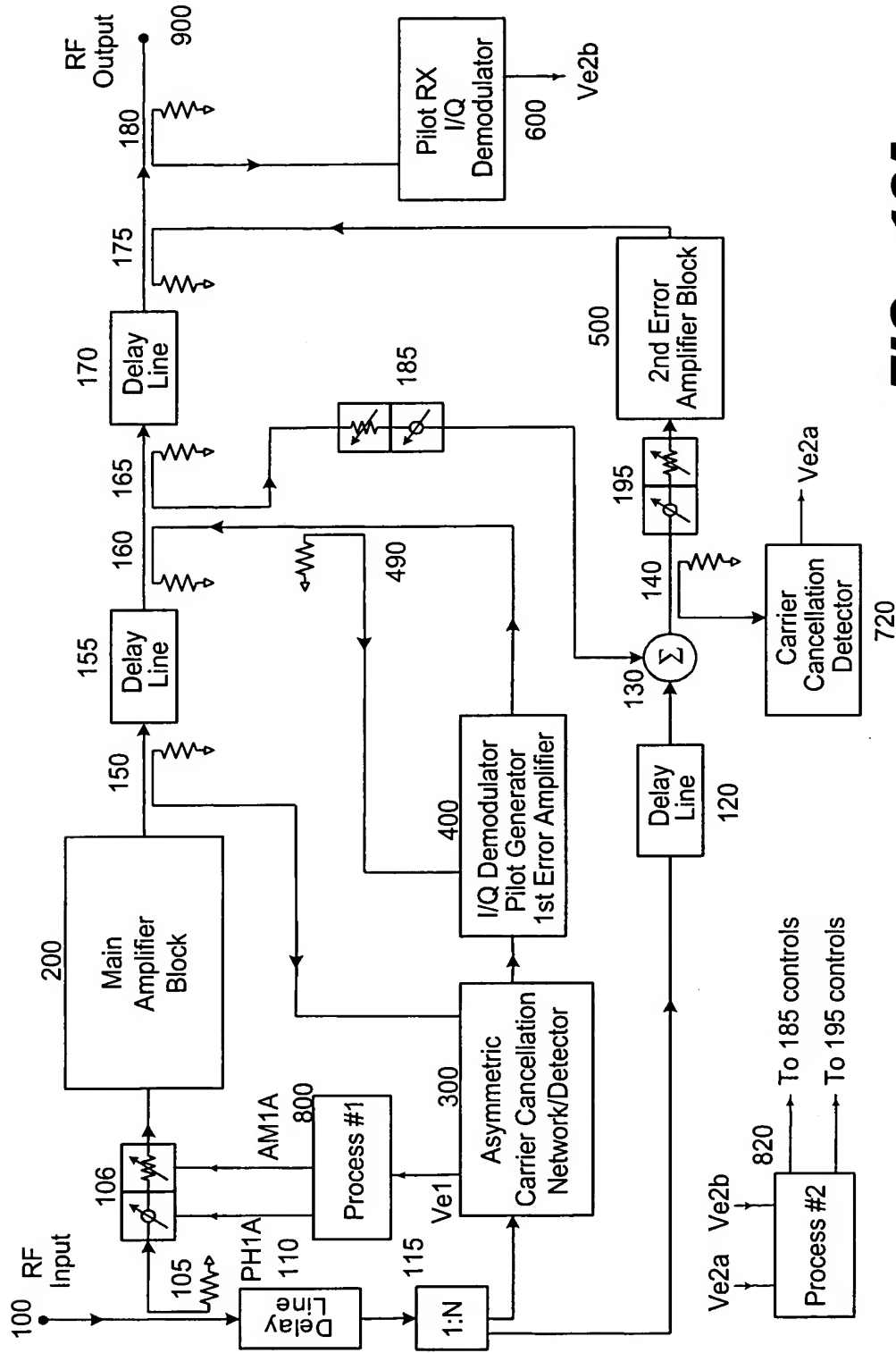


FIG. 13A



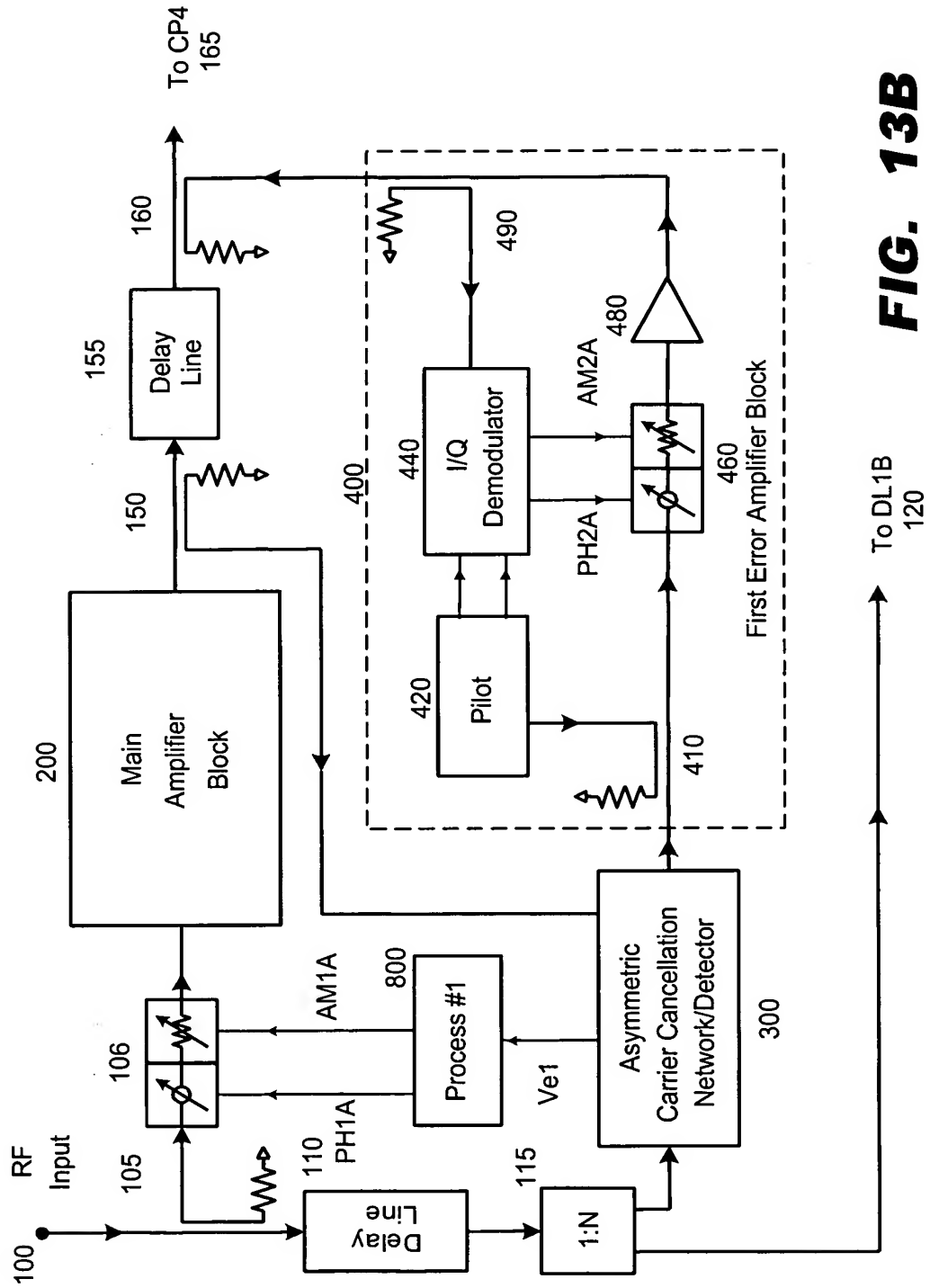


FIG. 13B

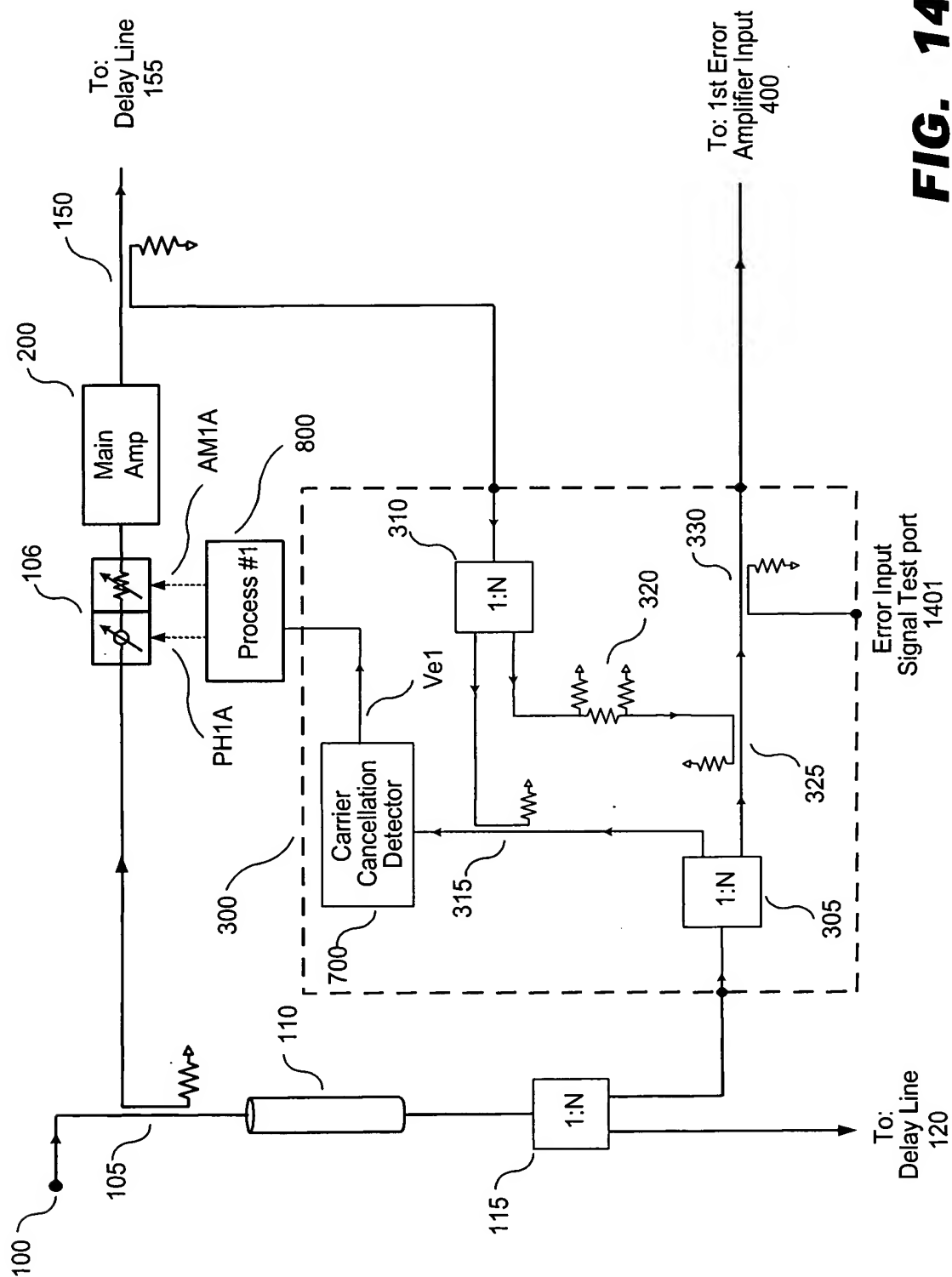


FIG. 14

FIG. 15